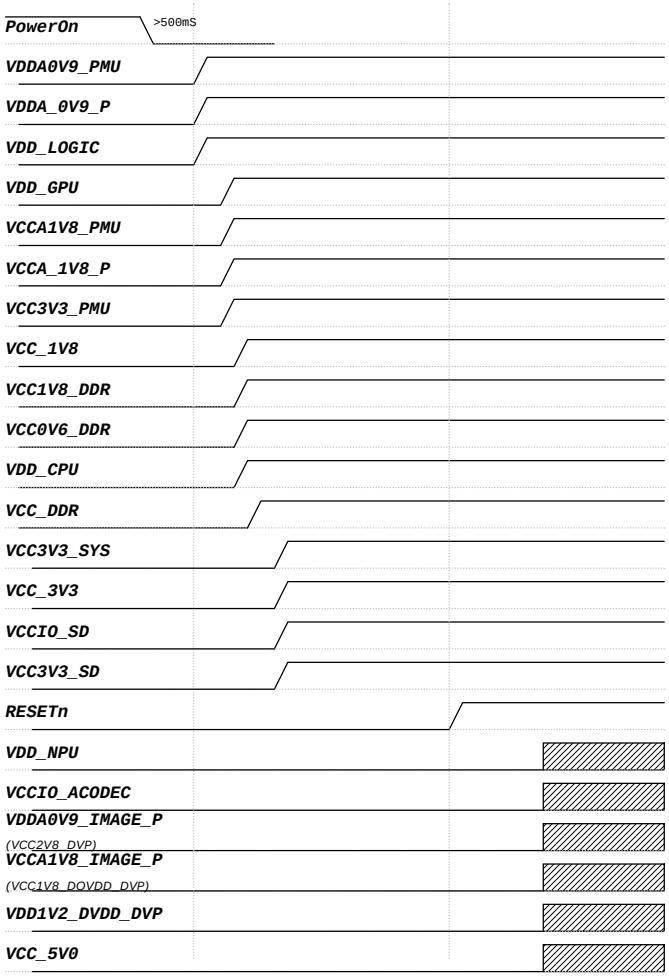
Revision History

Version	Date	By	Change Dsecription	Approved
V1.0	2022-07-01	Thomas	1: Revision preliminary version;	HYS
V1.1	2022-11-04	Thomas	1: Adjust some function settings and usages; 2: Modify eMMC power domain	HYS

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	00.Revision_History				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	1 of 20

Power Sequence(Use RK809-5)



Power description(Use RK809-5)

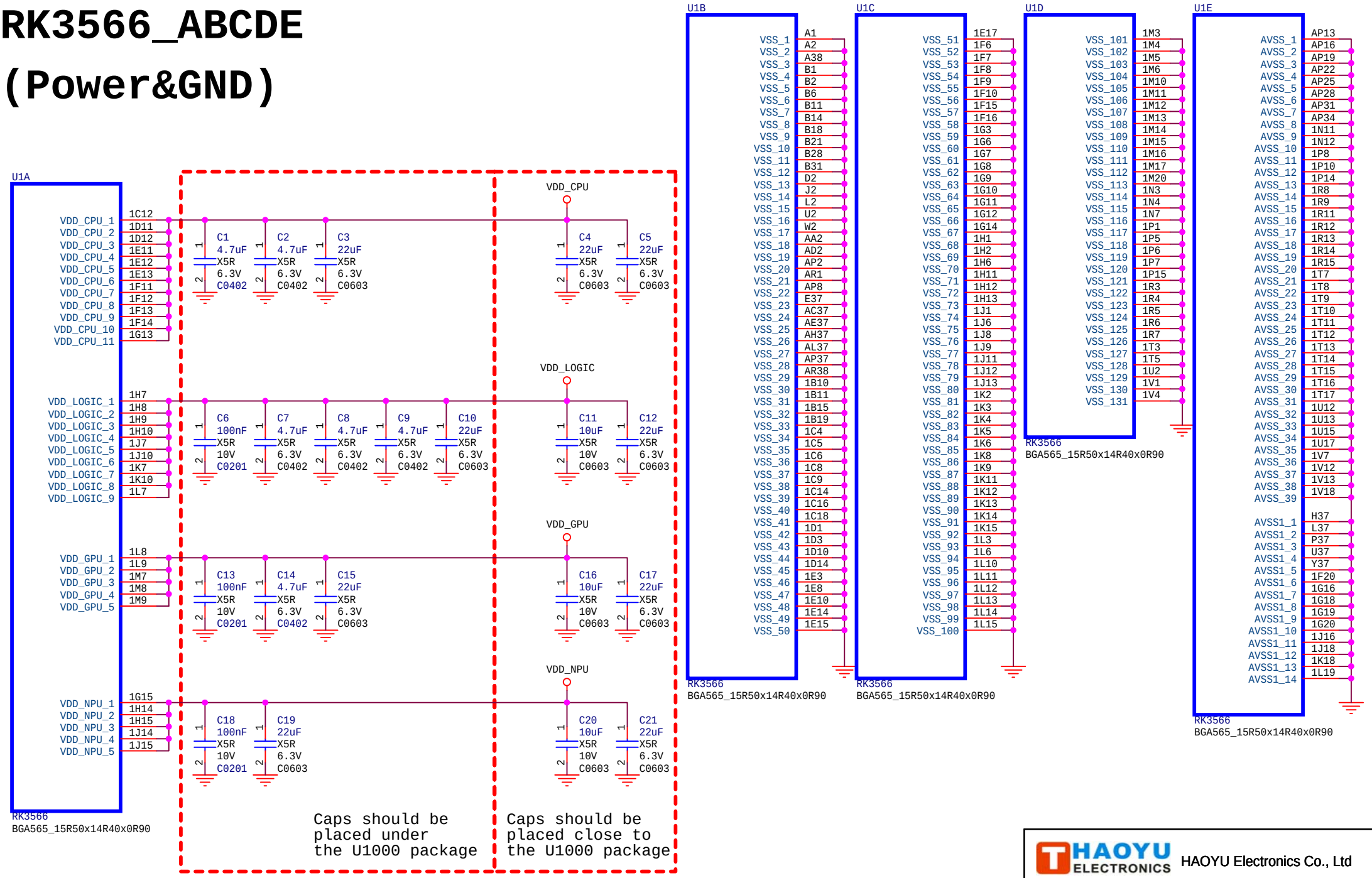
Power Supply	PMIC Channel	Supply Limit	Power Name	Time Slot	Default Voltage	Default ON/OFF	Work Voltage	Peak Current	Sleep Current
VCC_SYSIN	RK809_BUCK1	2.5A	VDD_LOGIC	Slot:1	0.9V	ON	0.9V	TBD	TBD
VCC_SYSIN	RK809_BUCK2	2.5A	VDD_GPU	Slot:2	0.9V	ON	DVFS	TBD	TBD
VCC_SYSIN	RK809_BUCK3	1.5A	VCC_DDR	Slot:3	ADJ FB=0.8V	ON	1.2V (DDR4)	TBD	TBD
VCC_SYSIN	RK809_BUCK4	1.5A	VDD_NPU	N/A	0V	OFF	DVFS	TBD	TBD
VCC_SYSIN	RK809_LDO1	0.4A	VDDA0V9_IMAGE_P (VCC2V8_DVP)	N/A	0V	OFF	2.8V (Camera)	TBD	TBD
	RK809_LDO2	0.4A	VDDA_0V9	Slot:1	0.9V	ON	0.9V	TBD	TBD
	RK809_LDO3	0.1A	VDDA0V9_PMU	Slot:1	0.9V	ON	0.9V	TBD	TBD
VCC_SYSIN	RK809_LDO4	0.4A	VCCIO_ACODEC	N/A	0V	OFF	3.3V	TBD	TBD
	RK809_LDO5	0.4A	VCCIO_SD	Slot:4	3.3V	ON	3.3V or 1.8V (SD2.0=3.3V,SD3.0=1.8V)	TBD	TBD
	RK809_LDO6	0.4A	VCC3V3_PMU	Slot:2	3.3V	ON	3.3V	TBD	TBD
VCC_SYSIN	RK809_LDO7	0.4A	VCCA_1V8	Slot:2	1.8V	ON	1.8V	TBD	TBD
	RK809_LDO8	0.4A	VCCA1V8_PMU	Slot:2	1.8V	ON	1.8V	TBD	TBD
	RK809_LDO9	0.4A	VCCA1V8_IMAGE_P (VCC1V8_DOVDD_DVP)	N/A	0V	OFF	1.8V (Camera)	TBD	TBD
VCC3V3_SYS	RK809_SW2 100mohm	2.1A	VCC3V3_SD	Slot:4	3.3V	ON	3.3V	TBD	TBD
VCC_SYSIN	RK809_SW1 90mohm	2.1A	N/A						
	RK809_BUCK5	2.5A	VCC_1V8	Slot:2	1.8V	ON	1.8V	TBD	TBD
	RK809_RESETn			Slot:4+5					
VCC_SYSIN	EXT BUCK	5.0A	VDD_CPU	Slot:2A	1.025V	ON	DVFS	TBD	TBD
VCC_SYSIN	EXT LDO	0.3A	VCC1V8_DDR	Slot:2A	1.8V	ON	1.8V	TBD	TBD
VCC_SYSIN	EXT BUCK	2.0A	VCC0V6_DDR	Slot:2A	0.6V	ON	0.6V	TBD	TBD
VCC3V3_SYS	EXT MOS		VCC_3V3	Slot:4	3.3V	ON	3.3V	TBD	TBD
VCC_SYSIN	EXT LDO or BUCK	0.5A /1.0A	VDD1V2_DVDD_DVP	N/A	0V	OFF	1.2V (Camera)	TBD	TBD
VCC_SYSIN	EX BOOST	1.5A	VCC_5V0	N/A	0V	OFF	5.0V	TBD	TBD

Check the software configuration(dts)
of voltage level, which must be
keep the same as hardware design

!!! Attention

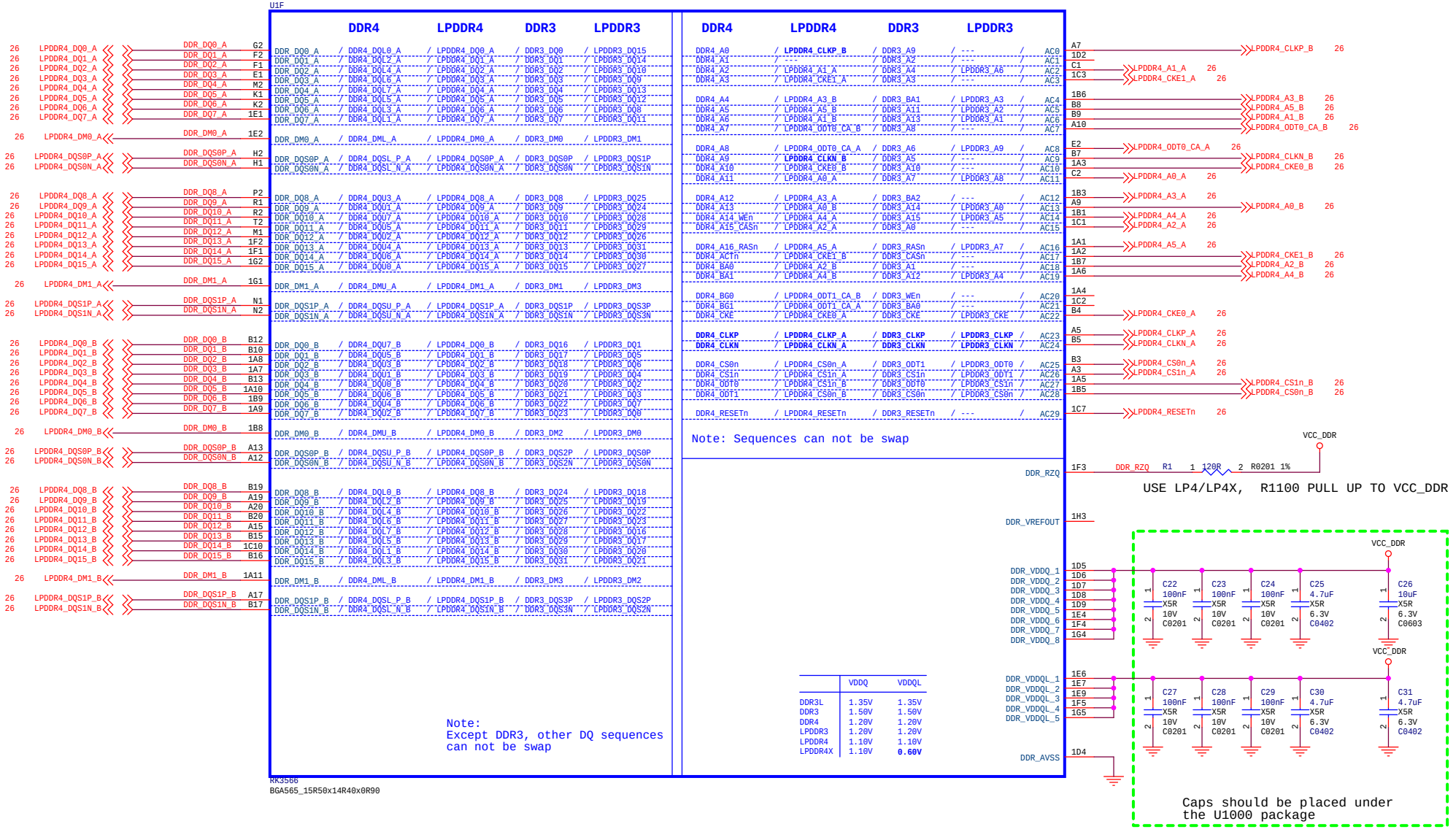
HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	01.Power_Sequence/IO_Domain_Map				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	2 of 20

RK3566_ABCDE (Power&GND)



HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	02.RK3566_Power/GND				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	3 of 20

RK3566_F(DDR PHY)



HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd
Project:	MarsBoard_CM4_SODIMM
File:	03.RK3566_DDR PHY
Date:	Friday, November 04, 2022
Rev:	V1.0
Designed by:	Thomas
Reviewed by:	Thomas
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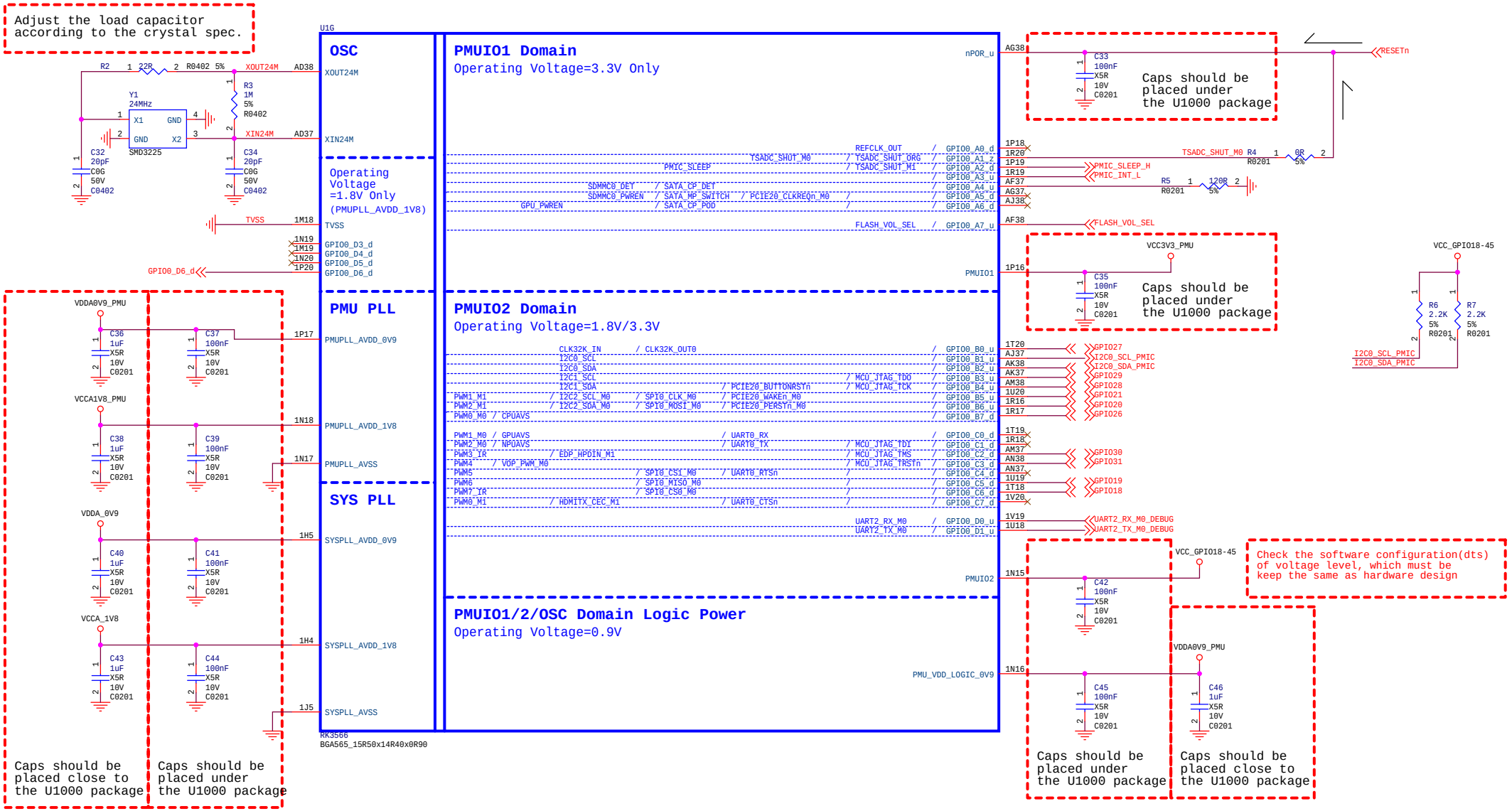
RK3566_G(OSC/PLL/PMUIO1/2)



THAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	04.RK3566_OSC/PLL/PMUIO				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	5 of 20

RK3566_I(VCCIO2 Domain)

U1I

VCCIO2 Domain

Operating Voltage=1.8V/3.3V

EMMC_D0	/ FLASH_D0	/	GPIO1_B4_u	A32
EMMC_D1	/ FLASH_D1	/	GPIO1_B5_u	B27
EMMC_D2	/ FLASH_D2	/	GPIO1_B6_u	B32
EMMC_D3	/ FLASH_D3	/	GPIO1_B7_u	B29
EMMC_D4	/ FLASH_D4	/	GPIO1_C0_u	B33
EMMC_D5	/ FLASH_D5	/	GPIO1_C1_u	A30
EMMC_D6	/ FLASH_D6	/	GPIO1_C2_u	B30
EMMC_D7	/ FLASH_D7	/	GPIO1_C3_u	A33
EMMC_CMD	/ FLASH_WRn	/	GPIO1_C4_u	A27
EMMC_CLKOUT	/ FLASH_DQS	/	GPIO1_C5_d	A29
EMMC_DATA_STROBE	/ FSPI_CS1n / FLASH_CLE	/	GPIO1_C6_d	1A16
EMMC_RSTn	/ FSPI_D2 / FLASH_WPn	/	GPIO1_C7_d	1B16
	FSPI_CLK / FLASH_ALE	/	GPIO1_D0_d	1A15
	FSPI_D0 / FLASH_RDY	/	GPIO1_D1_u	1A17
	FSPI_D1 / FLASH_RDn	/	GPIO1_D2_u	1A18
	FSPI_CS0n / FLASH_CS0n	/	GPIO1_D3_u	1B17
	FSPI_D3 / FLASH_CS1n	/	GPIO1_D4_u	1C15

Default is determined by Pin
FLASH_VOL_SEL/GPIO0_A7_u:
L:VCCIO2 must supply 3.3V
H:VCCIO2 must supply 1.8V

VCCIO2

RK3566
BGA565_15R50x14R40x0R90

A32 eMMC_D0
B27 eMMC_D1
B32 eMMC_D2
B29 eMMC_D3
B33 eMMC_D4
A30 eMMC_D5
B30 eMMC_D6
A33 eMMC_D7
A27 eMMC_CMD
A29 R8 1 22R 2 R0201 5% eMMC_CLKOUT
1A16 eMMC_DATA_STROBE
1B16 FSPI_D2
1A15 FSPI_CLK
1A17 FSPI_D0
1A18 FSPI_D1
1B17 FSPI_CS0n
1C15 FSPI_D3

1C13 VCCIO_FLASH
C47 100nF X5R 10V C0201
Caps should be placed under the U1000 package

Check the software configuration(dts) of voltage level, which must be keep the same as hardware design

RK3566_J(VCCIO3 Domain)

U1J

VCCIO3 Domain

Operating Voltage=1.8V/3.3V

PWM8_M1 / SDMMC0_D0	/ UART2_TX_M1 / UART6_TX_M1	/	GPIO1_D5_u	1E20
PWM9_M1 / SDMMC0_D1	/ UART2_RX_M1 / UART6_RX_M1	/	GPIO1_D6_u	1F19
SDMMC0_D2	/ ARM_JTAG_TCK/ UART5_CTSn_M0	/	GPIO1_D7_u	1D20
SDMMC0_D3	/ ARM_JTAG_TMS/ UART5_RTSn_M0	/	GPIO2_A0_u	1F18
PWM10_M1 / SDMMC0_CMD	/ UART5_RX_M0	/	GPIO2_A1_u	1E19
SDMMC0_CLK	/ TEST_CLKOUT / UART5_TX_M0	/	GPIO2_A2_d	G38

VCCIO3

RK3566
BGA565_15R50x14R40x0R90

1E20 SDMMC0_D0
1F19 SDMMC0_D1
1D20 SDMMC0_D2
1F18 SDMMC0_D3
1E19 SDMMC0_CMD
G38 R9 1 22R 2 R0201 5% SDMMC0_CLK

1F17 VCCIO_SD
C48 100nF X5R 10V C0201
Caps should be placed under the U1000 package

Check the software configuration(dts) of voltage level, which must be keep the same as hardware design

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	05.RK3566_Flash/SD_Controller				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	6 of 20

RK3566_U(USB3.0/PCIe2.0x1/SATA)

U1U

USB2.0 OTG_0
HS/FS/LS
(Download Port)

USB_OTG0_DP
USB_OTG0_DM
USB_OTG0_VBUSDET
USB_OTG0_ID

R37 USB_OTG0_DP
R38 USB_OTG0_DM
T38 USB_OTG0_VBUSDET
T37 USB_OTG0_ID

Diff 90ohm ± 10%

C49 100nF X5R 10V C0201

USB2.0 HOST_1
HS/FS/LS
(DP/DM for USB3.0 HOST)

USB_HOST1_DP
USB_HOST1_DM

1J19 USB3_HOST1_DP
1J20 USB3_HOST1_DM

Diff 90ohm ± 10%

USB2.0 Power
(for OTG_0 and HOST_1)

USB_AVDD1_0V9
USB_AVDD1_1V8
USB_AVDD1_3V3

1J17 USB_AVDD_0V9
1H16 USB_AVDD_1V8
1H18 VCC_3V3

C50 100nF X5R 10V C0201
C51 100nF X5R 10V C0201
C52 100nF X5R 10V C0201

Caps should be placed under the U1000 package

MULTI PHY U_S
(SS for USB3.0 HOST or SATA3_1)

USB3_HOST1_SSTXp/SATA1_TXp
USB3_HOST1_SSTXn/SATA1_TXn
USB3_HOST1_SSRXp/SATA1_RXp
USB3_HOST1_SSRXn/SATA1_RXn

W37 USB3_HOST1_SSTXP
W38 USB3_HOST1_SSTXN
V38 USB3_HOST1_SSRXP
V37 USB3_HOST1_SSRXN

Diff 90ohm ± 10%
Diff 90ohm ± 10%

MULTI PHY P_S
(PCIE2.0 or SATA3_2)

PCIE20_TXp/SATA2_TXp
PCIE20_TXn/SATA2_TXn
PCIE20_RXp/SATA2_RXp
PCIE20_RXn/SATA2_RXn
PCIE20_REFCLKp
PCIE20_REFCLKn

AA37 PCIE20_TXP
AA38 PCIE20_TXN
AB37 PCIE20_RXP
AB38 PCIE20_RXN
1K19 PCIE20_REFCLKP
1K20 PCIE20_REFCLKN

Diff 85ohm ± 10%
Diff 85ohm ± 10%
Diff 100ohm ± 10%

MULTI PHY Power
(for MULTI PHY U_S and P_S)

MULTI_PHY_AVDD_0V9
MULTI_PHY_AVDD_1V8

1K16 VDDA_0V9
1K17 VCCA_1V8

C53 100nF X5R 10V C0201
C54 100nF X5R 10V C0201

Caps should be placed under the U1000 package

C55 4.7uF X5R 6.3V C0402
C56 4.7uF X5R 6.3V C0402

RK3566
BGA565_15R50x14R40x0R90

RK3566_V(USB2.0 HOST)

U1V

USB2.0
(for HOST_2 and HOST_3)

USB_HOST2_DP
USB_HOST2_DM
USB_HOST3_DP
USB_HOST3_DM

V2
V1
Y2
Y1

VDDA_0V9
USB_AVDD2_0V9
1J4

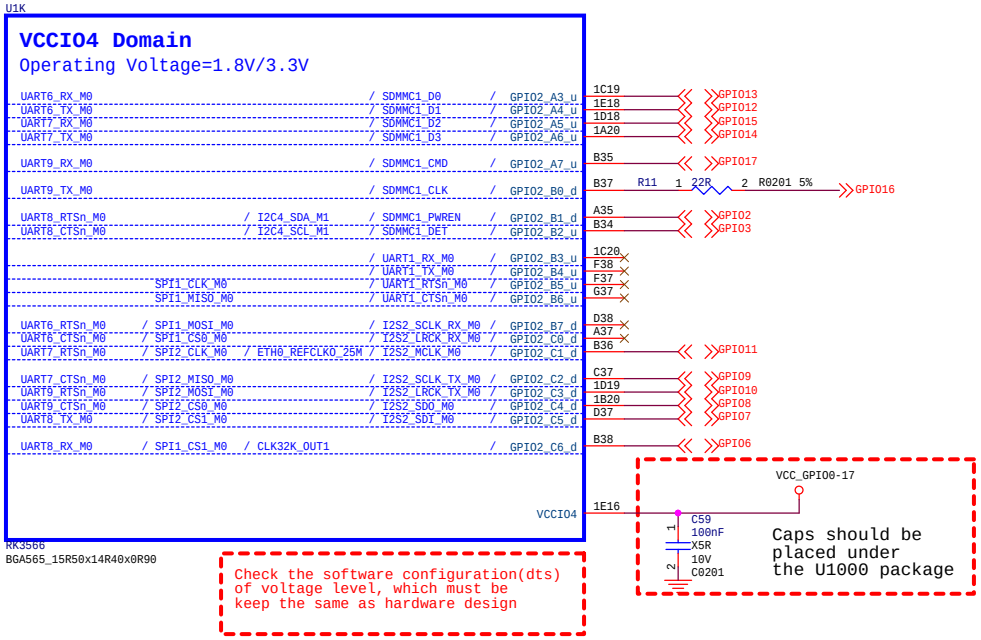
VCCA_1V8
USB_AVDD2_1V8
1J3

VCC_3V3
USB_AVDD2_3V3
1J2

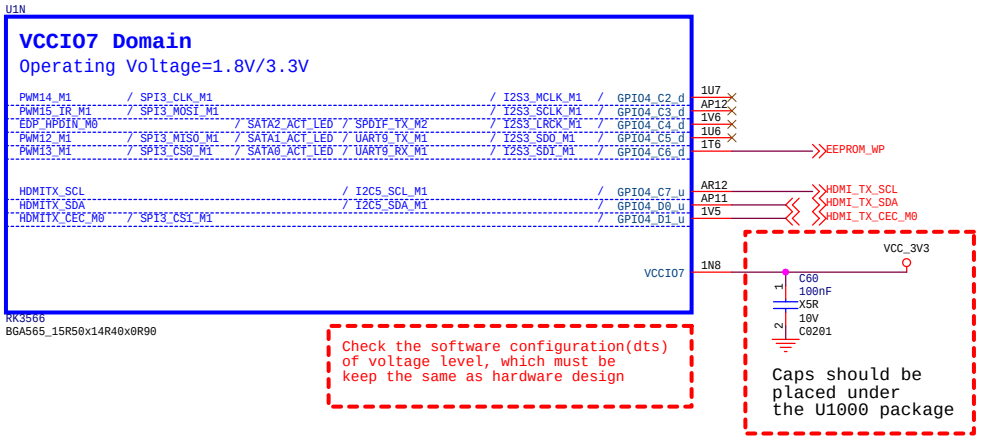
RK3566
BGA565_15R50x14R40x0R90

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	06.RK3566_USB/PCIe/SATA_PHY				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	7 of 20

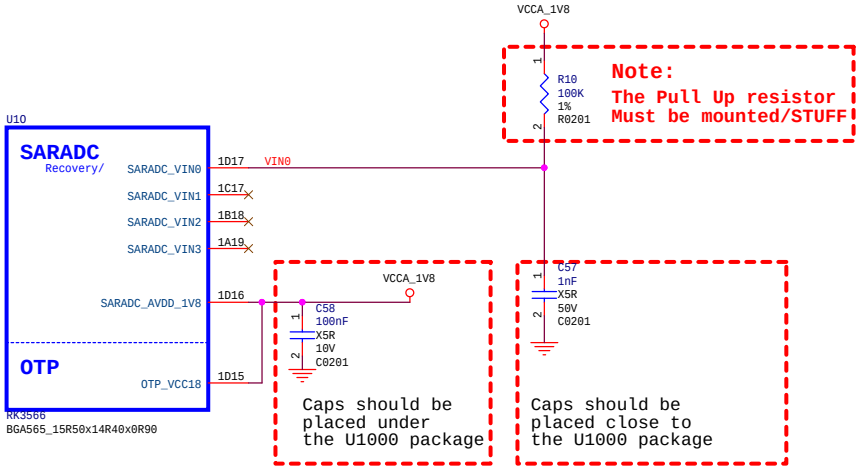
RK3566_K(VCCIO4 Domain)



RK3566_N(VCCIO7 Domain)



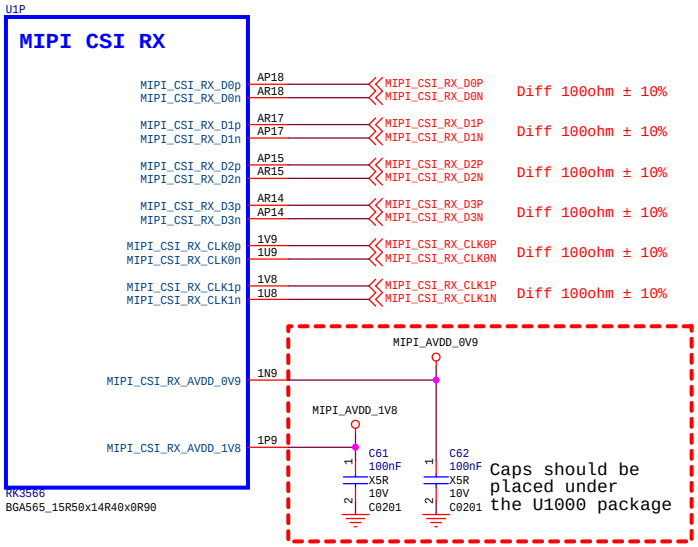
RK3566_O(SARADC/OTP)



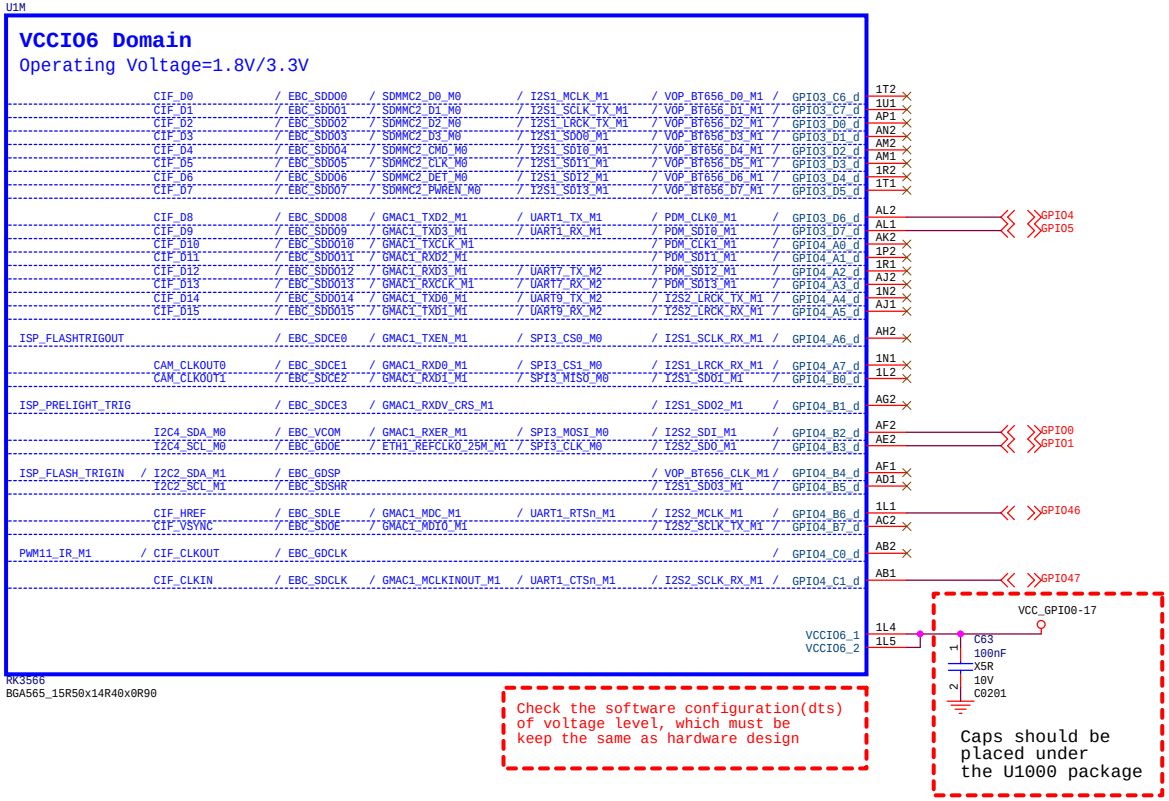
HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	07.RK3566_SARADC/GPIO				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	8 of 20

RK3566_P(MIPI_CSI_RX)

Usage of MIPI CSI Dx&CLKs		
Option1	Sensor1 x4Lane	MIPI_CSI_RX_D0-3 MIPI_CSI_RX_CLK0
Option2	Sensor1 x2Lane + Sensor2 x2Lane	MIPI_CSI_RX_D0-1 MIPI_CSI_RX_CLK0 MIPI_CSI_RX_D2-3 MIPI_CSI_RX_CLK1



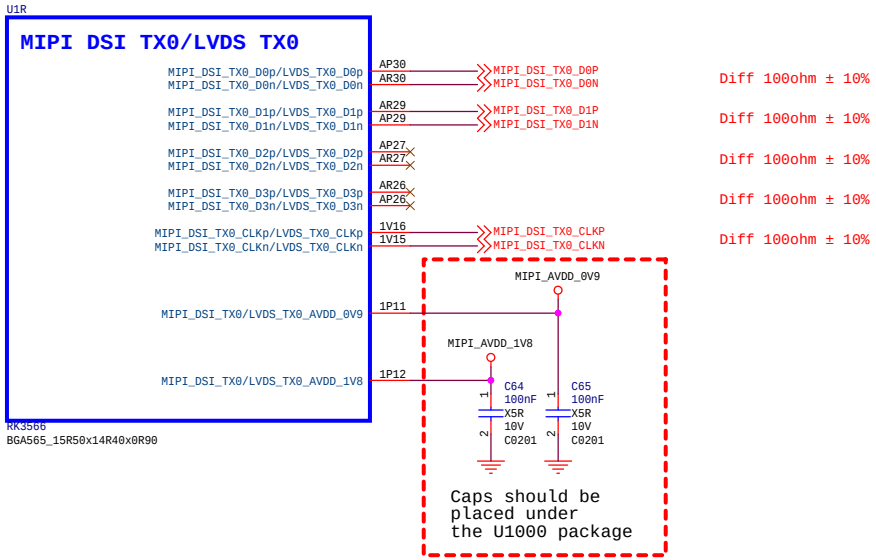
RK3566_M(VCCIO6 Domain)



Note:
default VCCIO of Camera is 1.8V

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	08.RK3566_VI_Interface				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	9 of 20

RK3566_R(MIPI_DSI_TX0/LVDS_TX0)



Diff 100ohm ± 10%

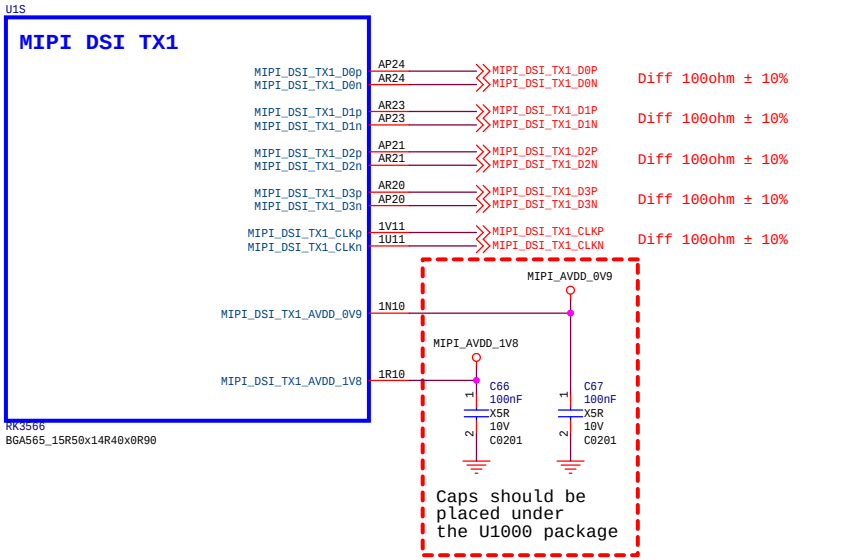
Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

RK3566_S(MIPI_DSI_TX1)



Diff 100ohm ± 10%

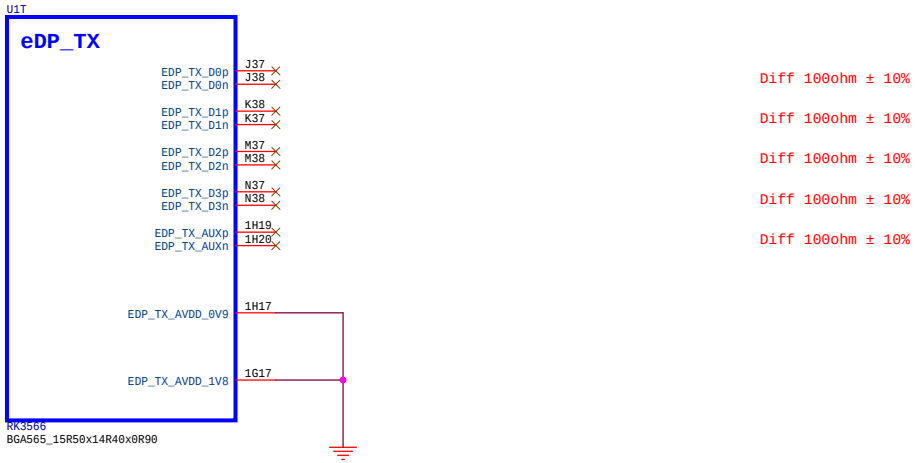
Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

RK3566_T(eDP TX)



Diff 100ohm ± 10%

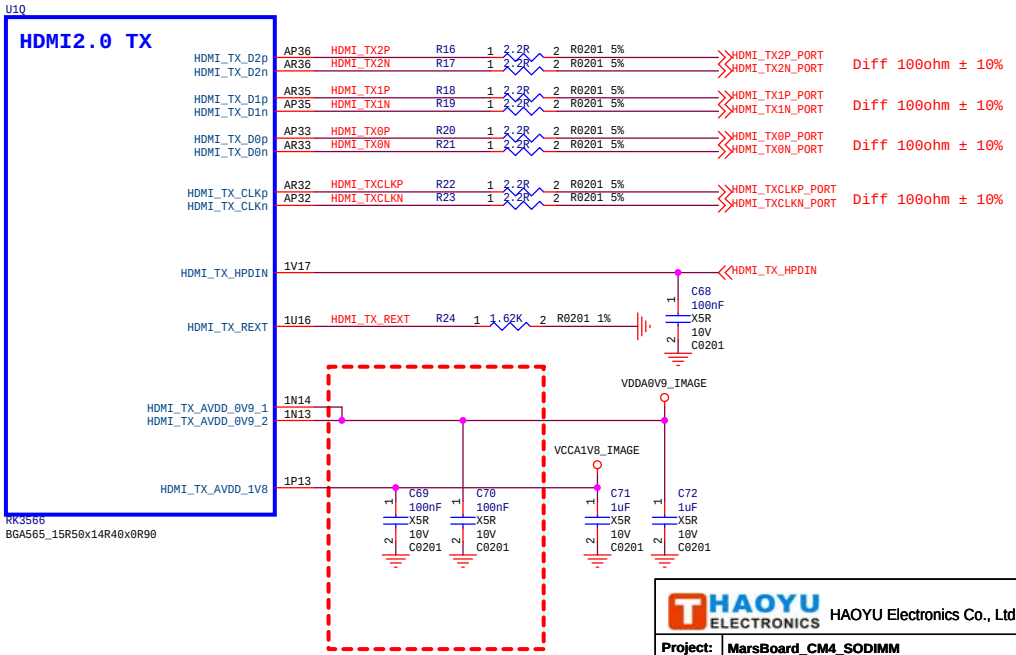
Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

RK3566_Q(HDMI2.0 TX)



Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

Diff 100ohm ± 10%

HAOYU ELECTRONICS HAOYU Electronics Co., Ltd

Project:	MarsBoard_CM4_SODIMM				
File:	09.RK3566_VO_Interface_1				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	10 of 20

RK3566_L(VCCIO5 Domain)

U1L

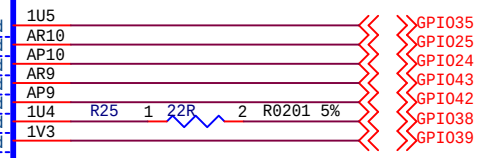
VCCIO5 Domain

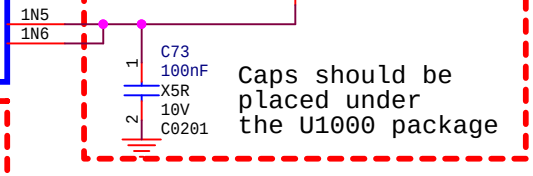
Operating Voltage=1.8V/3.3V

	VOP_BT1120_D0	/ SPI1_CS0_M1			/ SDMMC2_D0_M1	/ GPIO3_A1_d
	VOP_BT1120_D1		/ GMAC1_TXD2_M0	/ I2S3_MCLK_M0	/ SDMMC2_D1_M1	/ GPIO3_A2_d
	VOP_BT1120_D2		/ GMAC1_TXD3_M0	/ I2S3_SCLK_M0	/ SDMMC2_D2_M1	/ GPIO3_A3_d
	VOP_BT1120_D3		/ GMAC1_RXD2_M0	/ I2S3_LRCK_M0	/ SDMMC2_D3_M1	/ GPIO3_A4_d
	VOP_BT1120_D4		/ GMAC1_RXD3_M0	/ I2S3_SDO_M0	/ SDMMC2_CMD_M1	/ GPIO3_A5_d
	VOP_BT1120_CLK		/ GMAC1_TXCLK_M0	/ I2S3_SDI_M0	/ SDMMC2_CLK_M1	/ GPIO3_A6_d
	VOP_BT1120_D5		/ GMAC1_RXCLK_M0		/ SDMMC2_DET_M1	/ GPIO3_A7_d
	VOP_BT1120_D6		/ ETH1_REFCLKO_25M_M0		/ SDMMC2_PWREN_M1	/ GPIO3_B0_d
PWM8_M0	/ VOP_BT1120_D7		/ GMAC1_RXD0_M0	/ UART4_RX_M1		/ GPIO3_B1_d
PWM9_M0	/ VOP_BT1120_D8		/ GMAC1_RXD1_M0	/ UART4_TX_M1		/ GPIO3_B2_d
	VOP_BT1120_D9	/ I2C5_SCL_M0	/ GMAC1_RXDV_CRS_M0		/ PDM_SDI0_M2	/ GPIO3_B3_d
	VOP_BT1120_D10	/ I2C5_SDA_M0	/ GMAC1_RXER_M0		/ PDM_SDI1_M2	/ GPIO3_B4_d
PWM10_M0	/ VOP_BT1120_D11	/ I2C3_SCL_M1	/ GMAC1_TXD0_M0			/ GPIO3_B5_d
PWM11_IR_M0	/ VOP_BT1120_D12	/ I2C3_SDA_M1	/ GMAC1_TXD1_M0			/ GPIO3_B6_d
PWM12_M0			/ GMAC1_TXEN_M0	/ UART3_TX_M1	/ PDM_SDI2_M2	/ GPIO3_B7_d
PWM13_M0			/ GMAC1_MCLKINOUT_M0	/ UART3_RX_M1	/ PDM_SDI3_M2	/ GPIO3_C0_d
	VOP_BT1120_D13	/ SPI1_MOSI_M1		/ PCIE20_PERSTn_M1	/ I2S1_SDO2_M2	/ GPIO3_C1_d
	VOP_BT1120_D14	/ SPI1_MISO_M1		/ UART5_TX_M1	/ I2S1_SDO3_M2	/ GPIO3_C2_d
	VOP_BT1120_D15	/ SPI1_CLK_M1		/ UART5_RX_M1	/ I2S1_SCLK_RX_M2	/ GPIO3_C3_d
PWM14_M0		/ VOP_PWM_M1	/ GMAC1_MDC_M0	/ UART7_TX_M1	/ PDM_CLK1_M2	/ GPIO3_C4_d
PWM15_IR_M0		/ SPDIF_TX_M1	/ GMAC1_MDIO_M0	/ UART7_RX_M1	/ I2S1_LRCK_RX_M2	/ GPIO3_C5_d

VCCIO5_1
VCCIO5_2

RK3566
BGA565_15R50x14R40x0R90

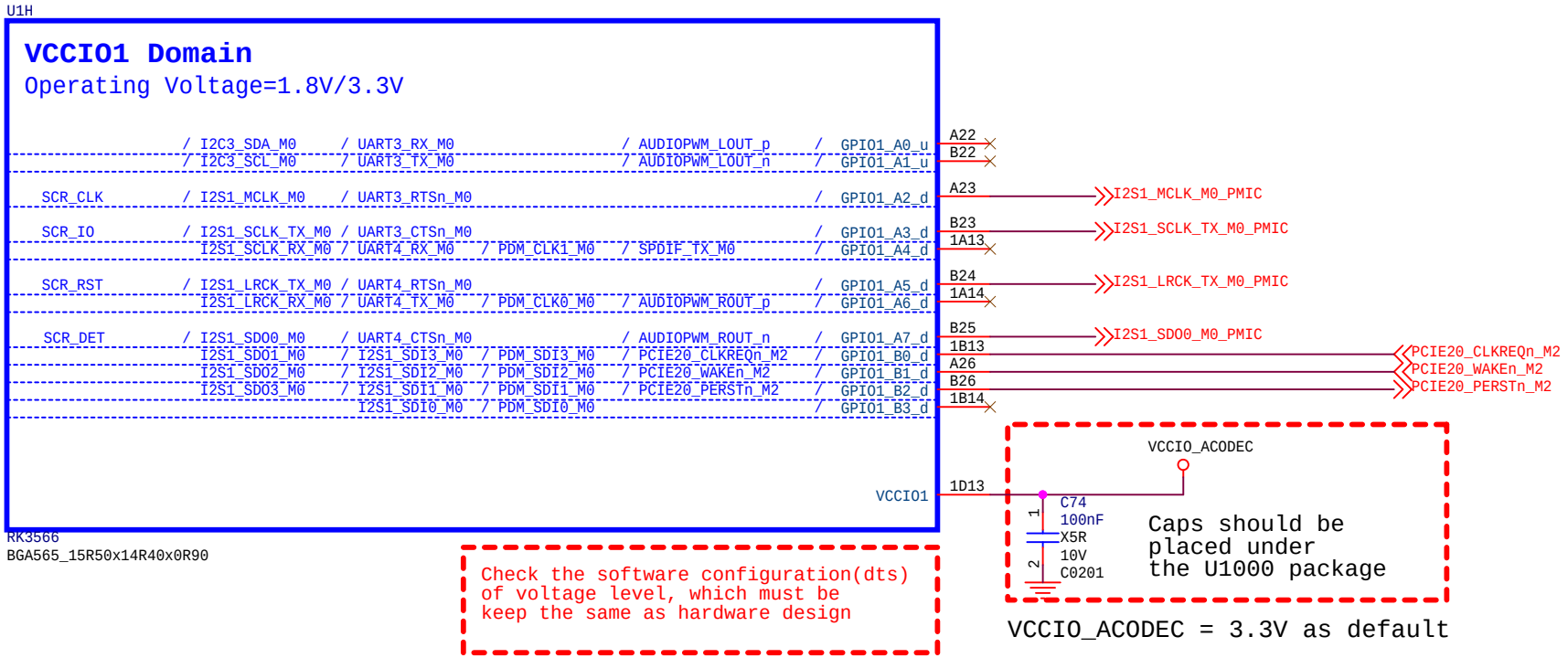




Check the software configuration(dts)
of voltage level, which must be
keep the same as hardware design

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	10.RK3566_VO_Interface_2				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	11 of 20

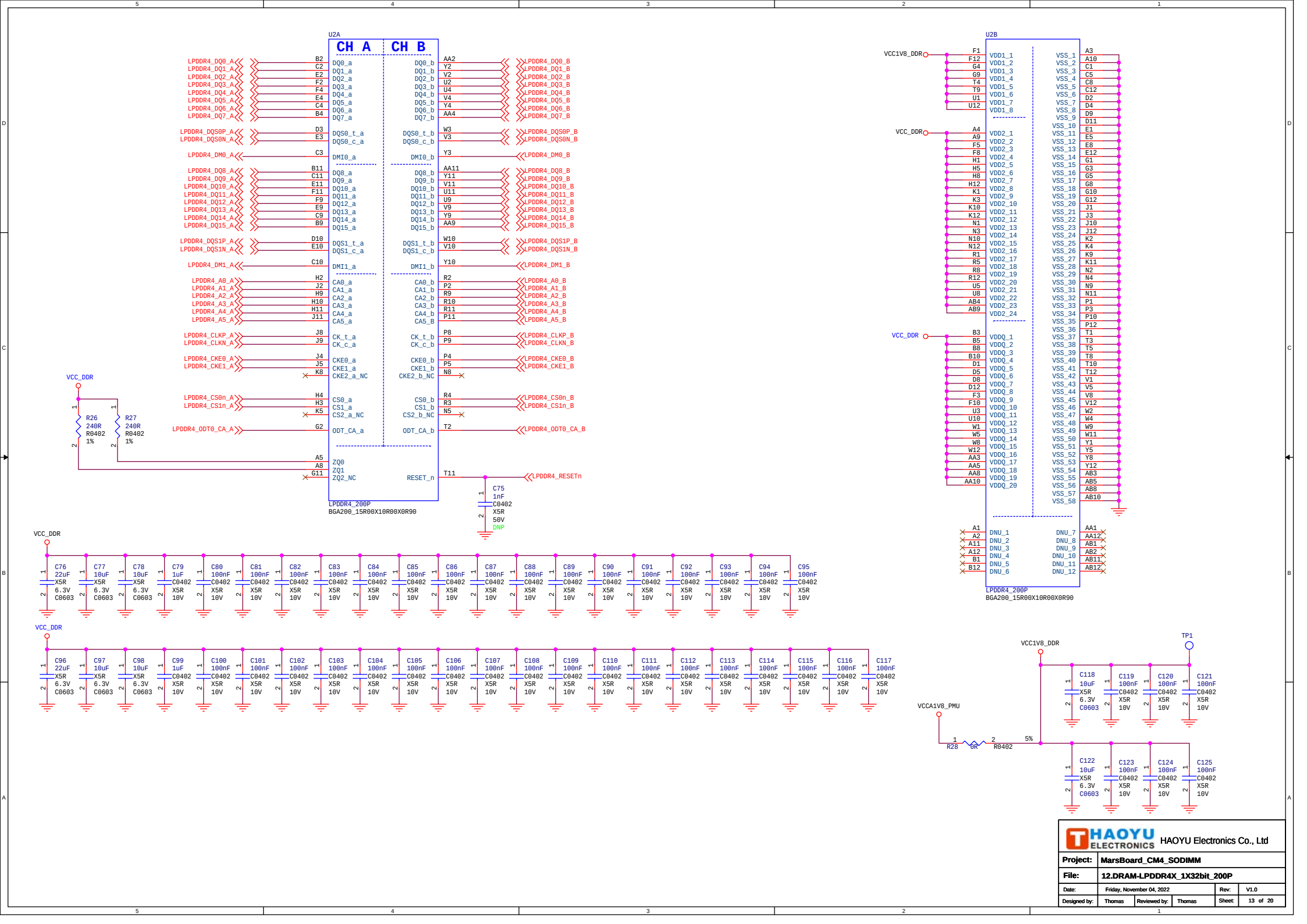
RK3566_H(VCCIO1 Domain)



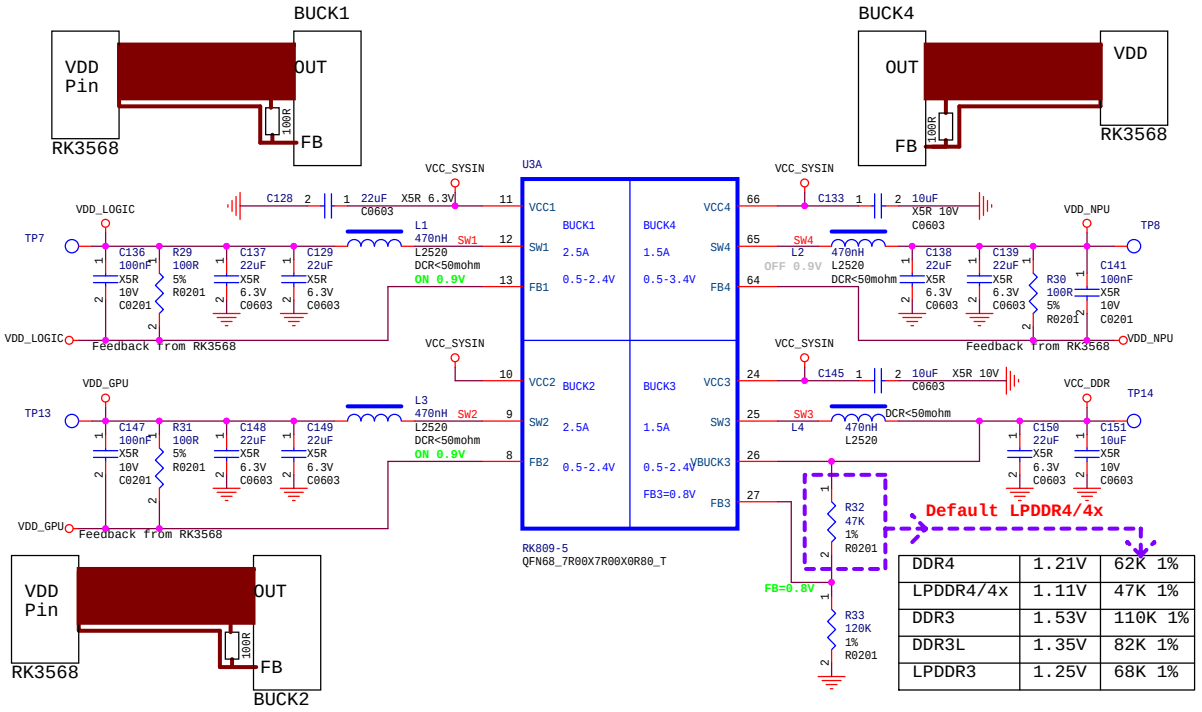
Check the software configuration(dts) of voltage level, which must be keep the same as hardware design

VCCIO_ACODEC = 3.3V as default

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	11.RK3566_Audio_Interface				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	12 of 20

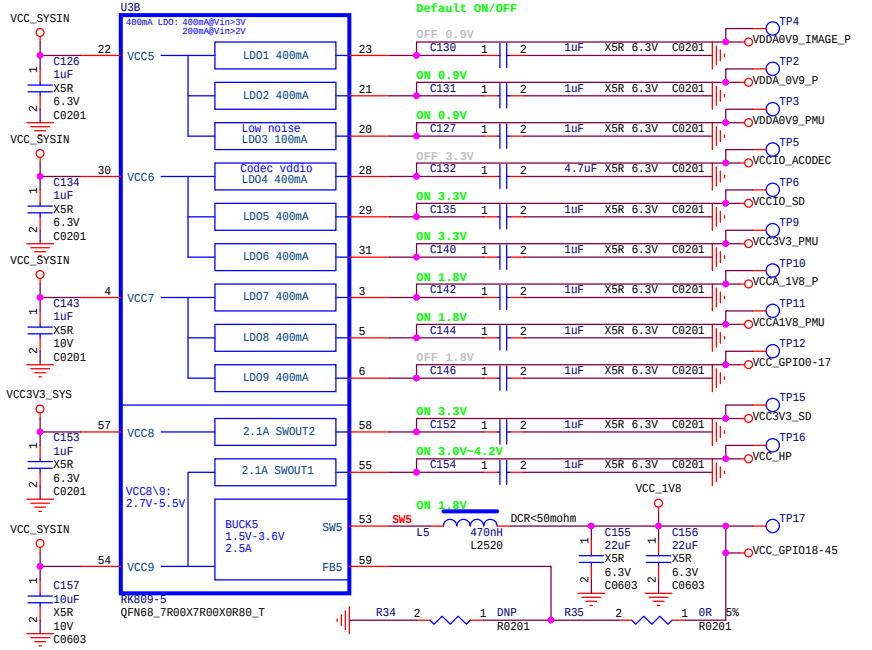

U2A
CH A
CH B
LPDDR4_200P
BGA200_15R00X10R00X0R90
U2B
LPDDR4_200P
BGA200_15R00X10R00X0R90
VCC_DDR
VCC1V8_DDR
VCCA1V8_PMU
R26 240R R0402 1%
R27 240R R0402 1%
R28 0R R0402 5%
C75 1nF C0402 X5R 50V DNP
TP1
HAOYU ELECTRONICS
HAOYU Electronics Co., Ltd
Project: MarsBoard_CM4_SODIMM
File: 12.DRAM-LPDDR4X_1X32bit_200P
Date: Friday, November 04, 2022
Rev: V1.0
Designed by: Thomas
Reviewed by: Thomas
Sheet: 13 of 20

PMIC RK809 DCDC

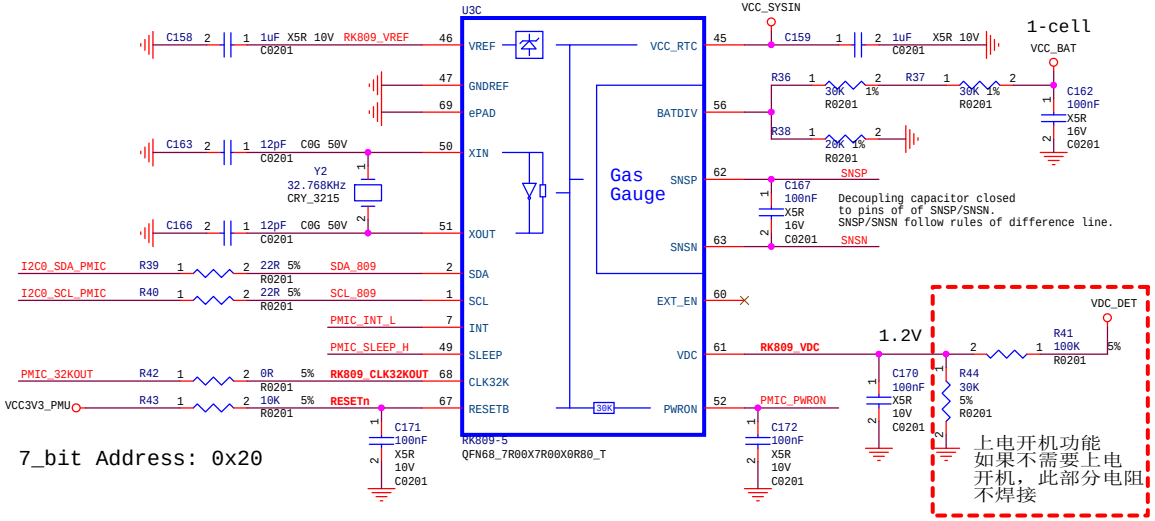


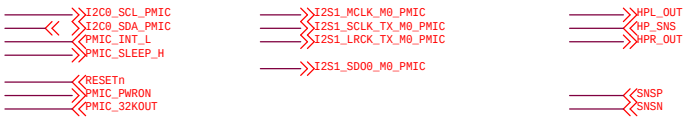
DDR4	1.21V	62K 1%
LPDDR4/4x	1.11V	47K 1%
DDR3	1.53V	110K 1%
DDR3L	1.35V	82K 1%
LPDDR3	1.25V	68K 1%

PMIC RK809 LDO

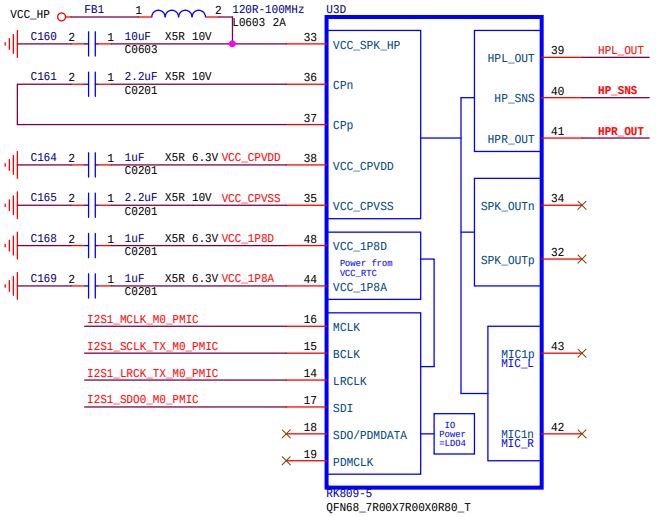


PMIC RK809 Managerment





PMIC RK809 CODEC

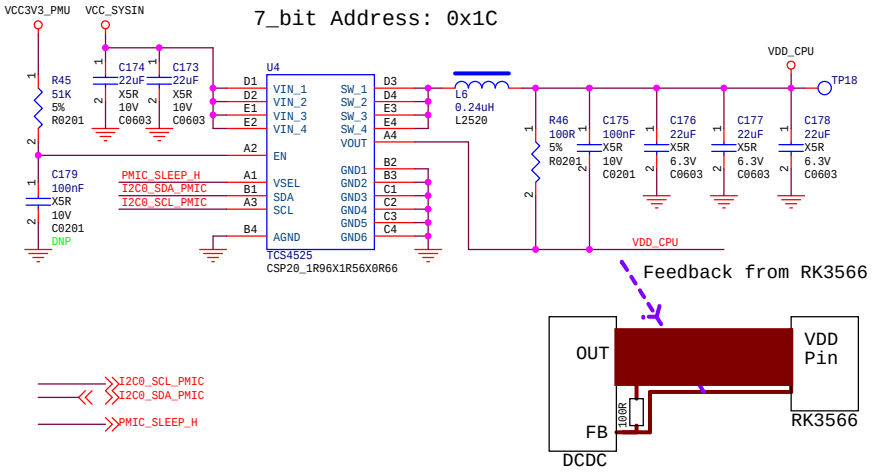


Note:

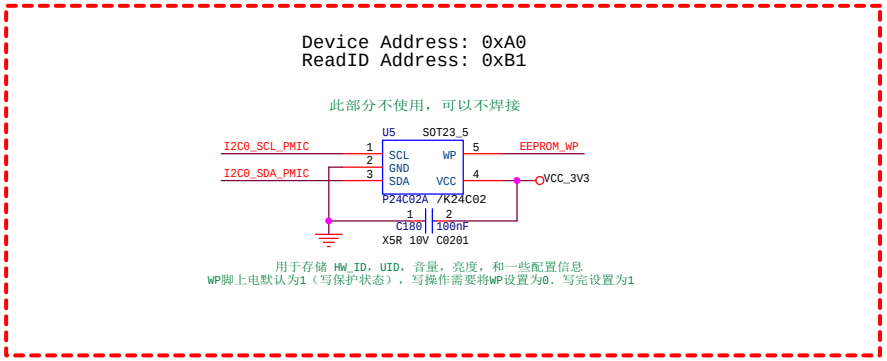
If RK809-5 codec is not used,
then Pin 14,15,16,17,19,40 Tie VSS
Pin 18,36,37,38,35,39,41,34,32,43,42
Leave floating

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	13.Power_Part3_PMIC				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	14 of 20

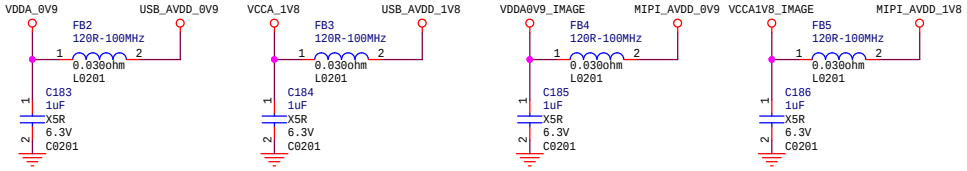
VDD_CPU_EXT



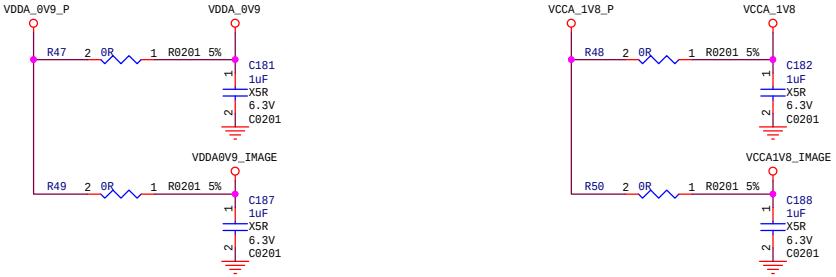
EEPROM



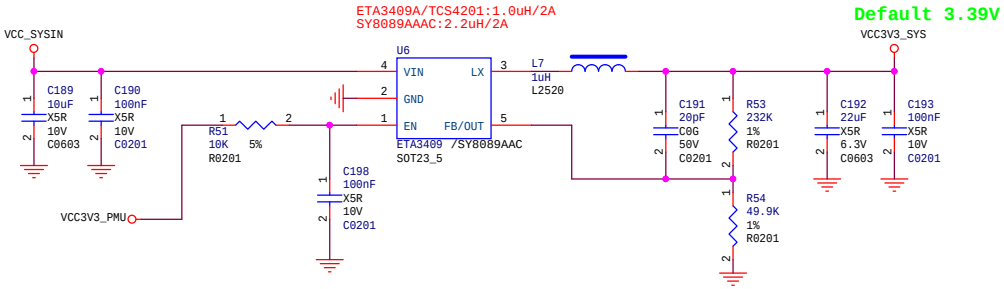
Power for USB / MIPI



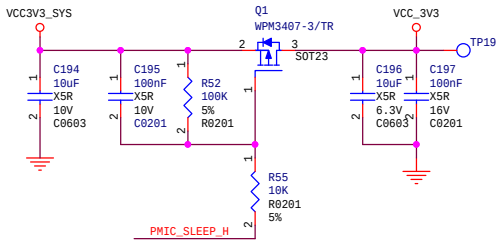
VDDA_0V9,VDDA0V9_IMAGE VCCA_1V8, VCCA1V8_IMAGE



VCC3V3_SYS



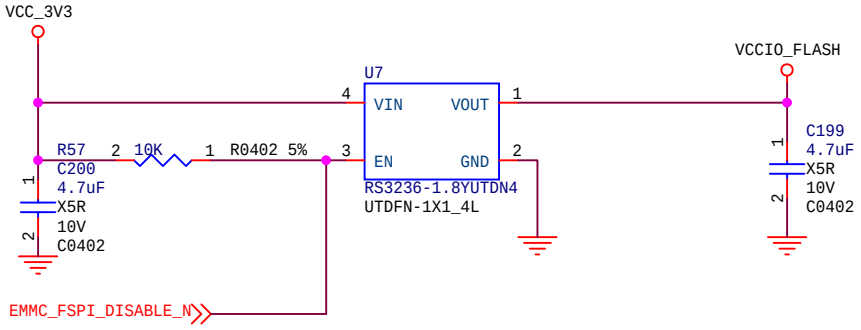
VCC_3V3 (Power OFF under SLEEP)



HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	14.Power_Part4_Extension				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	15 of 20

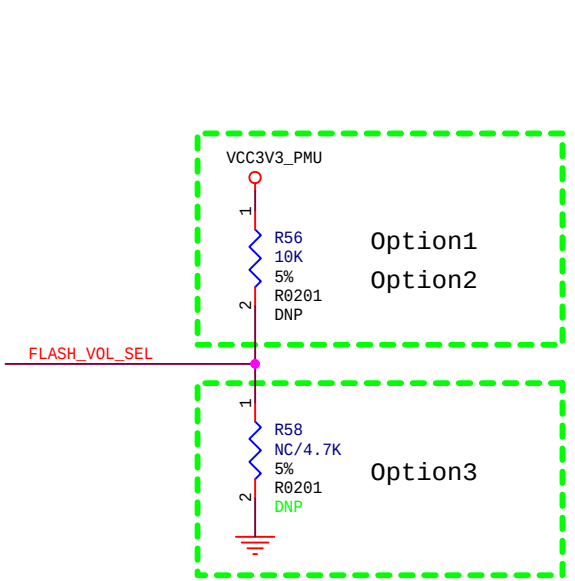
Flash Power Manage

	VCCIO2 domain voltage: Recommend voltage value (VCCIO_FLASH)	FLASH_VOL_SEL state decided to VCCIO2 domain IO driven by default
eMMC	1.8V	FLASH_VOL_SEL --> Logic=H
Nand flash	Default 3.3V, Adjust according to demand 1.8V	FLASH_VOL_SEL --> Logic=L(Default)
SPI flash	Default 3.3V, Adjust according to demand 1.8V	FLASH_VOL_SEL --> Logic=L(Default)



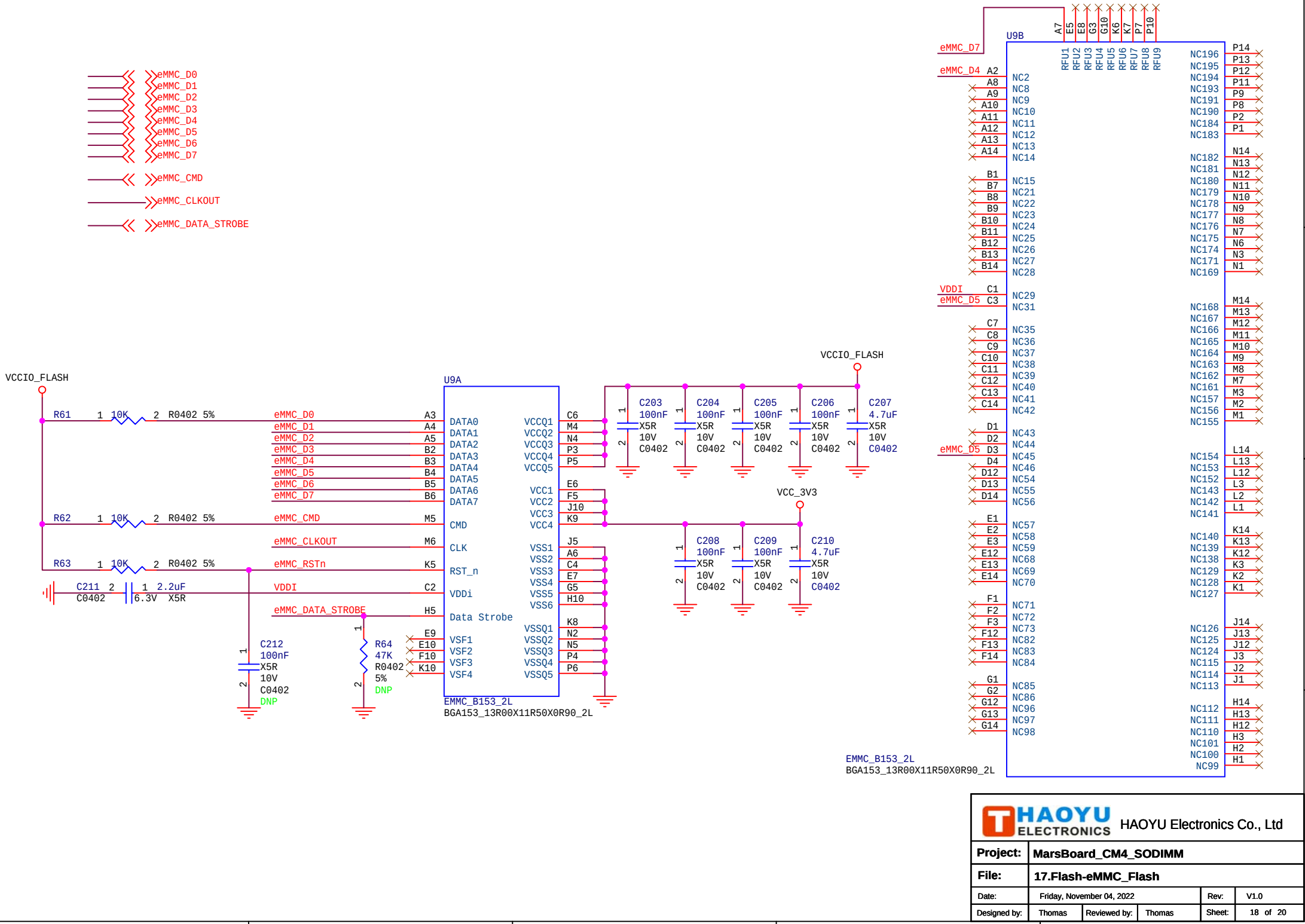
1. eMMC:
   Short 'EMMC_FSPI_DISABLE_N' and GND to enter Maskrom Mode.

2. SPI Flash:
   Short 'EMMC_FSPI_DISABLE_N' and GND to enter Maskrom Mode.



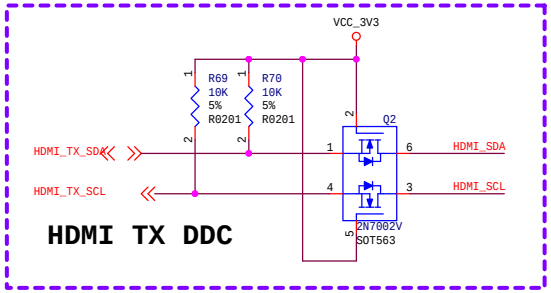
FLASH_VOL_SEL state decided
to VCCIO2 domain IO driven by default
Logic=L: 3.3V IO driven
Logic=H: 1.8V IO driven

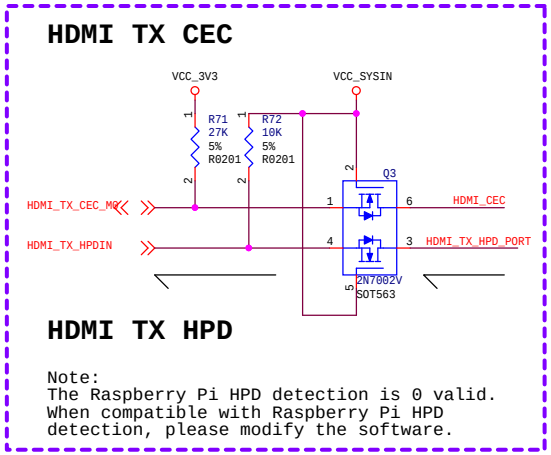
HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	15.Power_Flash_Power_Manage				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	16 of 20

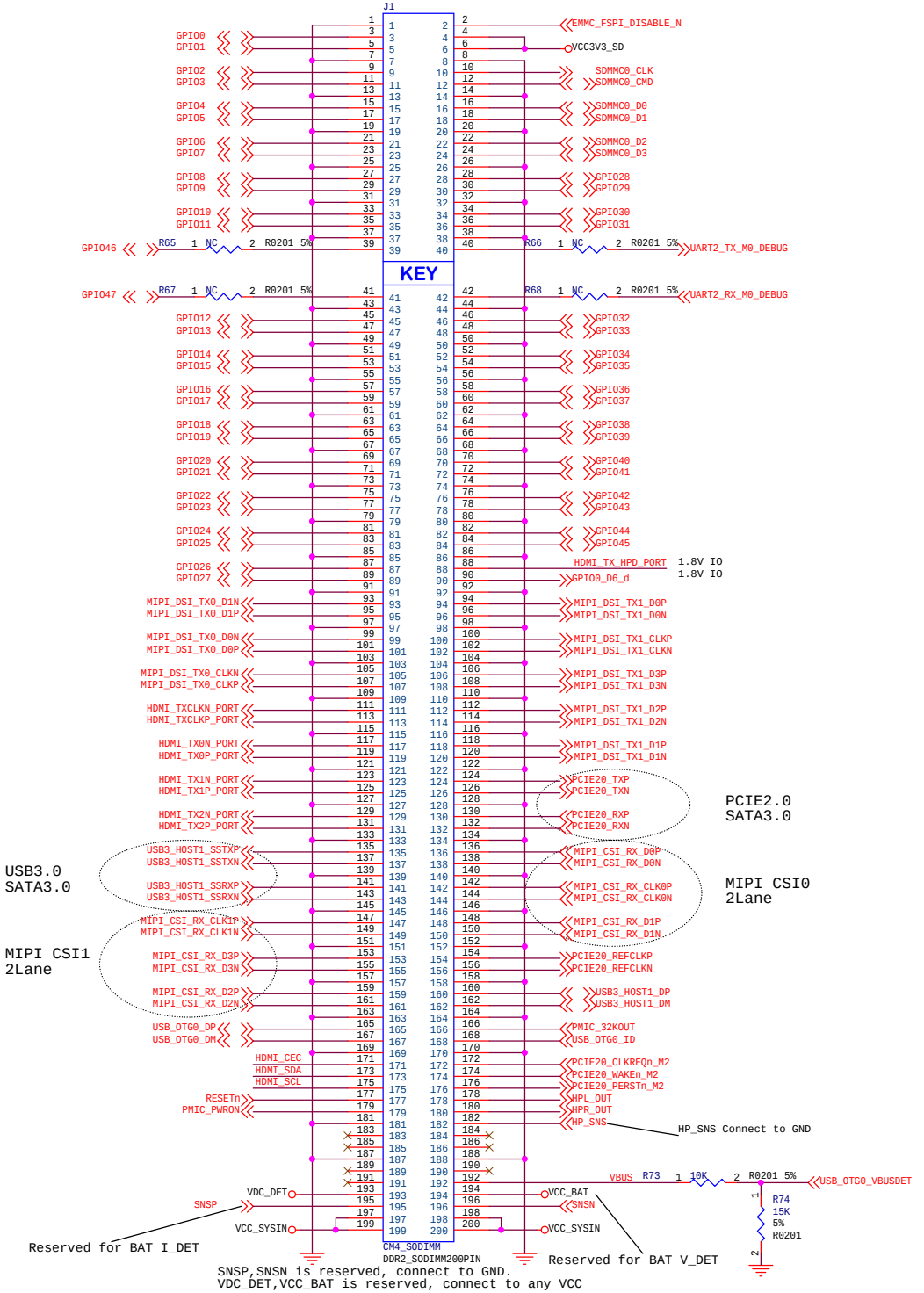


HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	17.Flash-eMMC_Flash				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	18 of 20

CM4 SODIMM PINOUT







HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	18.SODIMM_Pinout				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	19 of 20

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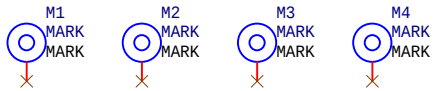
PCB Mark Point



Mechanical Hole

HAOYU ELECTRONICS	HAOYU Electronics Co., Ltd				
Project:	MarsBoard_CM4_SODIMM				
File:	19.Mark/Hole/Heatsink				
Date:	Friday, November 04, 2022			Rev:	V1.0
Designed by:	Thomas	Reviewed by:	Thomas	Sheet:	20 of 20